ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating junction temperature range, otherwise specifications are at T_A = 25°C. $V_A^+ = V_D^+ = V_{IN}^+ = V_{OUT0}^+ = V_{OUT1}^+ = V_{OUT2}^+$ = 3.3V, unless otherwise specified. All voltages are with respect to GND. (Note 2)

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
LVDS Clock Outputs							
f_{OUT}	Frequency	Differential Termination = 100Ω, 3.5mA Mode	●			800	MHz
		Differential Termination = 50Ω, 7mA Mode	●			1400	MHz
$\|V_{OD}\|$	Differential Voltage (Output Static)	Differential Termination = 100Ω, 3.5mA Mode	●	290	370	450	mV_{PK}
		Differential Termination = 50Ω, 7mA Mode	●	290	370	450	mV_{PK}
$\|\Delta V_{OD}\|$	Delta V_{OD} (Output Static)	Differential Termination = 100Ω, 3.5mA Mode	●	–30		30	mV
		Differential Termination = 50Ω, 7mA Mode	●	–30		30	mV
V_{OS}	Offset Voltage (Output Static)	Differential Termination = 100Ω, 3.5mA Mode	●	1.16	1.23	1.32	V
		Differential Termination = 50Ω, 7mA Mode	●	1.15	1.23	1.32	V
$\|\Delta V_{OS}\|$	Delta V_{OS} (Output Static)	Differential Termination = 100Ω, 3.5mA Mode	●	–15		15	mV
		Differential Termination = 50Ω, 7mA Mode	●	–15		15	mV
t_{RISE}	Rise Time, 20% to 80%	Differential Termination = 100Ω, 3.5mA Mode			240		ps
		Differential Termination = 50Ω, 7mA Mode			120		ps
t_{FALL}	Fall Time, 80% to 20%	Differential Termination = 100Ω, 3.5mA Mode			240		ps
		Differential Termination = 50Ω, 7mA Mode			120		ps
$\|I_{SA}\|$, $\|I_{SB}\|$	Short-Circuit Current to Common	Shorted to GND, 3.5mA Mode			16		mA
		Shorted to GND, 7mA Mode			25		mA
$\|I_{SAB}\|$	Short-Circuit Current to Complementary	3.5mA Mode			4		mA
		7mA Mode			8		mA
DC_{LVDS}	Duty Cycle	Mx[5:0] = 1			DC_{IN}		%
		Mx[5:0] > 1 (Even or Odd)	●	45	50	55	%
Output Propagation Delays							
$t_{PD(LVPECL)}$	Propagation Delay From IN to Any LVPECL Output	Mx[5:0] = 1	●	~~290~~ 250	360	480	ps
		Mx[5:0] > 1	●	~~360~~ 320	430	550	ps
	Temperature Variation of the Propagation Delay From IN to Any LVPECL Output	Mx[5:0] = 1	●		0.65		ps/°C
		Mx[5:0] > 1	●		0.68		ps/°C
$t_{pd(LVDS)}$	Propagation Delay From IN to Any LVDS Output, LVCSx = 1 (7mA Mode)	Mx[5:0] = 1	●	~~350~~ 305	420	545	ps
		Mx[5:0] > 1	●	~~415~~ 370	480	625	ps
	Temperature Variation of the Propagation Delay From IN to Any LVDS Output, LVCSx = 1 (7mA Mode)	Mx[5:0] = 1	●		0.8		ps/°C
		Mx[5:0] > 1	●		0.85		ps/°C
	Propagation Delay From IN to Any LVDS Output, LVCSx = 0 (3.5mA Mode)	Mx[5:0] = 1			480		ps
		Mx[5:0] > 1			550		ps
	Temperature Variation of the Propagation Delay From IN to Any LVDS Output, LVCSx = 0 (3.5mA Mode)	Mx[5:0] = 1	●		0.8		ps/°C
		Mx[5:0] > 1	●		0.85		ps/°C
$t_{pd(CMOS)}$	Propagation Delay From IN to Any CMOS Output, Complementary Outputs (CMSINVx = 1)	Mx[5:0] = 1			1.25		ns
		Mx[5:0] > 1			1.32		ns
	Temperature Variation of the Propagation Delay From IN to Any CMOS Output (CMSINVx = 1)	Mx[5:0] = 1	●		1.3		ps/°C
		Mx[5:0] > 1	●		1.4		ps/°C